Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

Claims 1-74 (Canceled)

75. (Previously Presented) An active termination circuit for setting the input impedance of a plurality of terminals to a predetermined value, the active termination circuit comprising:

a respective first controllable impedance device coupled between a first supply voltage and each of the terminals, the impedance of the first controllable impedance device being controlled by an impedance control signal;

a control circuit coupled to provide the impedance control signal to all of the first controllable impedance devices, the first control circuit comprising:

a second controllable impedance device coupled between a second supply voltage and a feedback node, the second controllable impedance device being a different controllable impedance device from the first controllable impedance devices, and the feedback node being different from one of the terminals, the impedance of the second controllable impedance device being controlled by the impedance control signal;

a predetermined resistance coupled between the feedback node and a third supply voltage, the second controllable impedance device and the predetermined resistance forming a voltage divider between the second and third supply voltages to produce a feedback voltage at the feedback node; and

a circuit generating the impedance control signal as a function of the feedback voltage so that magnitude of the feedback voltage is substantially constant.

76. (Previously Presented) The active termination circuit of claim 75 wherein the circuit generating the impedance control signal comprises a comparator circuit comparing the feedback voltage to a reference voltage, the comparator circuit causing the impedance control signal to vary so that the feedback voltage is substantially equal to the reference voltage.

77. (Previously Presented) The active termination circuit of claim 76 wherein the comparator circuit comprises a first differential amplifier generating a comparison signal corresponding to the difference between the feedback voltage and the reference voltage, the impedance control signal corresponding to the comparison signal.

78. (Previously Presented) The active termination circuit of claim 75 wherein the control circuit is operable to provide a common impedance control signal to all of the first controllable impedance devices.

79. (Previously Presented) The active termination circuit of claim 75 wherein the first and second supply voltages have the same magnitude.

80. (Previously Presented) The active termination circuit of claim 79 wherein the first and second supply voltages comprise a power supply voltage.

81. (Previously Presented) The active termination circuit of claim 80 wherein the third supply voltage comprises ground potential.

82. (Previously Presented) The active termination circuit of claim 75 wherein the control circuit is operable to generate the impedance control signal to maintain the impedance of the second controllable impedance device substantially equal to the impedance of the predetermined resistance.

83. (Previously Presented) The active termination circuit of claim 75 wherein the first controllable impedance device and the second controllable impedance device comprise identical controllable impedance devices.

84. (Previously Presented) The active termination circuit of claim 83 wherein the first controllable impedance device and the second controllable impedance device comprise identical MOSFET transistors.

85. (Previously Presented) The active termination circuit of claim 75 wherein the first and second controllable impedance devices comprises respective voltage controlled impedance devices.

86. (Previously Presented) The active termination circuit of claim 75 wherein the active termination circuit is fabricated in an integrated circuit device, and wherein the terminals having their impedance set by the active termination circuit comprise terminals that are externally accessible from outside the integrated circuit.

Claims 87-115 (Canceled)